NSN 5962-01-118-5890

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Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 2 input
Design Function And Quantity:
2 flip-flop, d-type
Test Data Document:
81349-mil-38510 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification
format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain
environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
A458a0