NSN 5962-01-118-7785

Memory Microcircuit - Page 1 of 1



View Online at https://aerobasegroup.com/nsn/5962-01-118-7785 **Overall Height:** 0.360 inches **Body Length:** Between 0.740 inches and 0.810 inches **Body Width:** Between 0.280 inches and 0.310 inches **Body Height:** 0.110 inches **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Burn in and hermetically sealed and schottky and w/totem pole output and programmable and bipolar **Inclosure Material:** Ceramic **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 10 input **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: 7.0 volts power source **Time Rating Per Chacteristic:** 40.00 nanoseconds propagation delay time, low to high level output and 40.00 nanoseconds propagation delay time, high to low level output **Memory Device Type:** Rom **Test Data Document:** 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). **Terminal Type And Quantity:** 16 printed circuit Shelf Life: N/a **Unit Of Measure:**

No Fiig:

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Demilitarization: