

View Online at <https://aerobasegroup.com/nsn/5962-01-119-4081>

Features Provided:

Bipolar and monolithic and positive outputs and w/independent outputs and w/totem pole output and 2-state output

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Hex 1 input

Design Function And Quantity:

6 gate, inverting

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

5.0 volts power source

Time Rating Per Characteristic:

22.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level output

Special Features:

Wide temperature range

Test Data Document:

05869-717123-5 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

14 flat leads

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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