# NSN 5962-01-121-0932

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# **Body Length:**

Between 0.745 inches and 0.770 inches

# Body Width:

Between 0.240 inches and 0.260 inches

# **Body Height:**

Between 0.120 inches and 0.160 inches

# **Maximum Power Dissipation Rating:**

200.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and high voltage and medium speed and low impedance and monolithic

#### Inclosure Material:

Ceramic

#### Inclosure Configuration:

Dual-in-line

## **Output Logic Form:**

Complementary-metal oxide-semiconductor logic

# Input Circuit Pattern:

Quad 2 input

#### **Design Function And Quantity:**

4 gate, exclusive or

#### Case Outline Source And Designator:

Mo-001-ad joint electron device engineering council

#### Shelf Life:

N/a

#### Unit Of Measure:

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#### Demilitarization:

Yes - demil/mli

#### Fiig:

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