NSN 5962-01-121-9606

Digital Microcircuit - Page 1 of 1



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Body Length:

0.390 inches

Body Width:

Between 0.235 inches and 0.260 inches

Body Height:

Between 0.045 inches and 0.085 inches

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

Features Provided:

W/open collector

Inclosure Material:

Ceramic

Inclosure Configuration:

Flat pack

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Quad 2 input

Design Function And Quantity:

1 buffer, nand quad

Case Outline Source And Designator:

F-2 mil-m-38510

Time Rating Per Chacteristic:

32.00 nanoseconds propagation delay time, low to high level output and 28.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

80063-sm-a-858546 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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