NSN 5962-01-121-9610

Digital Microcircuit - Page 1 of 2





Body Length:

0.785 inches

Body Width:

Between 0.220 inches and 0.310 inches

Body Height:

Between 0.140 inches and 0.185 inches

Maximum Power Dissipation Rating:

24.4 milliwatts

Operating Tempurature Range:

-55.0 to 125.0 degrees celsius

Storage Tempurature Range:

-65.0 to 150.0 degrees celsius

End Application:

Gmsrsmodkit12 81205

Features Provided:

Burn in and hermetically sealed and low power and monolithic and schottky

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

Quad 2 input

Design Function And Quantity:

4 gate, nand

Case Outline Source And Designator:

D-1 mil-m-38510

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

5.5 volts power source

Time Rating Per Chacteristic:

20.00 nanoseconds propagation delay time, low to high level output and 24.00 nanoseconds propagation delay time, high to low level output

Special Features:

Nuclear hardness critical item

Nuclear Hardness Critical Feature:

Hardened

Test Data Document:

81205-280-22065 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

14 printed circuit

NSN 5962-01-121-9610

Digital Microcircuit - Page 2 of 2

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

A458a0

