# NSN 5962-01-123-7987

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### **Body Length:**

0.440 inches

#### **Body Width:**

Between 0.240 inches and 0.325 inches

#### **Body Height:**

0.085 inches

#### **Maximum Power Dissipation Rating:**

910.0 milliwatts

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and programmable and bipolar and w/enable and 3-state output and w/buffered

### output

Inclosure Material:

### Ceramic and glass

#### **Inclosure Configuration:**

Flat pack

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

10 input

#### **Terminal Surface Treatment:**

Solder

#### Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

#### **Time Rating Per Chacteristic:**

45.00 nanoseconds propagation delay time, low to high level output and 89.00 nanoseconds propagation delay time, high to low level output

#### Memory Device Type:

Rom

#### **Memory Capacity:**

Unknown

#### Test Data Document:

82577-932820 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### Terminal Type And Quantity:

16 flat leads

#### Shelf Life:

N/a

#### Unit Of Measure:

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## Demilitarization:

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