# NSN 5962-01-125-1014

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# **Body Length:**

Between 1.235 inches and 1.285 inches

# **Body Width:**

Between 0.515 inches and 0.600 inches

# **Body Height:**

Between 0.145 inches and 0.187 inches

# **Maximum Power Dissipation Rating:**

525.0 milliwatts

#### **Operating Tempurature Range:**

+0.0/+70.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **End Application:**

An/tsc-6 (v) 1 fscm 99999

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and w/buffered output and ultraviolet erasable and w/enable and 3-state output

#### **Inclosure Material:**

Ceramic and glass

# Inclosure Configuration:

Dual-in-line

# **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

12 input

## **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

-1.5 volts power source and 5.5 volts power source

## Time Rating Per Chacteristic:

450.00 nanoseconds propagation delay time, low to high level output and 450.00 nanoseconds propagation delay time, high to low level output

#### Memory Device Type:

Rom

## Memory Capacity:

Unknown

#### **Terminal Type And Quantity:**

24 printed circuit

#### Shelf Life:

N/a

#### Unit Of Measure:

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### Demilitarization:

Yes - demil/mli

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