NSN 5962-01-126-9336

Memory Microcircuit - Page 1 of 1

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Body Length:
Between 1.180 inches and 1.220 inches
Body Width:
Between 0.560 inches and 0.600 inches
Body Height:
Between 0.080 inches and 0.130 inches
Maximum Power Dissipation Rating:
1.0 watts
Operating Tempurature Range:
-55.0/+70.0 degrees celsius
Storage Tempurature Range:
-65.0/+125.0 degrees celsius
Features Provided:
Monolithic and programmable and burn in
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line Dual-in-line
Output Logic Form:
Metal oxide-semiconductor logic
Input Circuit Pattern:
8 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-20.0 volts power source and 0.5 volts power source
Memory Device Type:
Rom
Test Data Document:
16340-64-00009 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
24 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig: