NSN 5962-01-127-1744

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Operating Tempurature Range:
+0.0/+70.0 degrees celsius
Features Provided:
Expandable and hermetically sealed
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
4 wide
Design Function And Quantity:
1 gate, and-or invert
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli

Fiig: A458a0