## NSN 5962-01-128-0528

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|---|
| Body Length:  |
| Between 0.740 inches and 0.770 inches   |
| Body Width:   |
| Between 0.240 inches and 0.260 inches   |
| Body Height:  |
| Between 0.130 inches and 0.140 inches   |
| Operating Tempurature Range:  |
| +0.0/+70.0 degrees celsius  |
| Storage Tempurature Range:  |
| -65.0/+150.0 degrees celsius  |
| Features Provided:  |
| Positive edge triggered and complementary outputs and electrostatic sensitive |
| Inclosure Material:   |
| Plastic   |
| Inclosure Configuration:  |
| Dual-in-line  |
| Output Logic Form:  |
| Transistor-transistor logic   |
| Input Circuit Pattern:  |
| Dual 4 input  |
| Design Function And Quantity:   |
| 2 flip-flop, edge triggered, d-type   |
| Voltage Rating And Type Per Characteristic:                                   |
| 7.0 volts power source and 5.5 volts input logic level                        |
| Terminal Type And Quantity:   |
| 14 printed circuit  |
| Shelf Life:   |
| N/a   |
| Unit Of Measure:  |
|   |
| Demilitarization:   |
| No  |
| Fiig:   |