## NSN 5962-01-128-3299

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Body Length:
Between 0.850 inches and 0.930 inches
Body Width:
Between 0.240 inches and 0.295 inches
Body Height:
0.200 inches
Operating Tempurature Range:
55.0/+125.0 degrees celsius
Storage Tempurature Range:
65.0/+150.0 degrees celsius
Features Provided:
Programmable and programmed and bipolar
nclosure Material:
Ceramic
nclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
nput Circuit Pattern:
12 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
60.00 nanoseconds propagation delay time, low to high level output and 60.00 nanoseconds propagation delay time, high to low level
putput
Memory Device Type:
Rom
Test Data Document:
09344-pc73618 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
18 printed circuit
Shelf Life:
N/a
Unit Of Measure:
-
Demilitarization:
Yes - demil/mli
Fiig: