NSN 5962-01-129-4191

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Body Length:
Between 0.371 inches and 0.400 inches
Body Width:
Between 0.247 inches and 0.285 inches
Body Height:
Between 0.050 inches and 0.080 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and w/open collector and positive outputs
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Quad 2 input
Design Function And Quantity:
4 gate, nand
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-1.5 volts power source and 5.5 volts power source
Time Rating Per Chacteristic:
45.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
16 flat leads
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli

Fiig:

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