# NSN 5962-01-130-8591

Digital Microcircuit - Page 1 of 1



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### **Body Length:**

Between 0.245 inches and 0.260 inches

### **Body Width:**

Between 0.245 inches and 0.260 inches

### **Body Height:**

Between 0.040 inches and 0.050 inches

### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

### **Features Provided:**

Hermetically sealed and w/resistor and monolithic

### **Inclosure Material:**

Ceramic or metal

### Inclosure Configuration:

Leadless flat pack

## Output Logic Form:

Transistor-transistor logic

### Input Circuit Pattern:

Quad 2 input

**Design Function And Quantity:** 

4 gate, nand

**Terminal Surface Treatment:** 

Solder

### Voltage Rating And Type Per Characteristic:

7.0 volts power source

### **Time Rating Per Chacteristic:**

15.00 nanoseconds propagation delay time, low to high level output and 15.00 nanoseconds propagation delay time, high to low level output

### **Test Data Document:**

96906-mil-std883 standard (includes industry or association standards, individual manufactureer standards, etc.).

### **Terminal Type And Quantity:**

18 leadless

Shelf Life:

N/a

### Unit Of Measure:

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### Demilitarization:

Yes - demil/mli

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