## NSN 5962-01-135-2105

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| Body Leng        |   |
|------------------|---|
| 1.310 inche      |   |
| Body Widtl       |   |
| 0.600 inche      |   |
| Body Heigl       |   |
| 0.052 inche      |   |
|                  | Power Dissipation Rating:   |
| 400.0 milliw     |   |
|                  | Tempurature Range:  |
|                  | degrees celsius   |
|                  | mpurature Range:  |
|                  | 0 degrees celsius   |
| End Applic       |   |
| Training dev     |   |
| Features P       |   |
|                  | / sealed and programmable and expandable and wire-or outputs and w/enable |
| Inclosure N      |   |
| Ceramic an       | -   |
| Inclosure C      | Configuration:  |
| Dual-in-line     |   |
| Output Log       | jic Form:   |
| Transistor-t     | ransistor logic   |
| Input Circu      | it Pattern:   |
| 8 input          |   |
| Terminal S       | urface Treatment:   |
| Solder           |   |
| Voltage Ra       | ting And Type Per Characteristic:   |
| -1.5 volts p     | ower source and 5.5 volts power source                                    |
| Time Ratin       | g Per Chacteristic:   |
| 75.00 nanos      | seconds propagation delay time, low to high level output                  |
| Memory De        | evice Type:   |
| Rom              |   |
| Memory Ca        | pacity:   |
| Unknown          |   |
| Terminal T       | ype And Quantity:   |
| 24 printed c     | ircuit  |
| Shelf Life:      |   |
| N/a              |   |
| Unit Of Mea      | asure:  |
| <br>Demilitariza | ation   |
|                  | imli  |

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**Fiig:** A458a0

