# NSN 5962-01-139-4252

Digital Microcircuit - Page 1 of 1



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# **Body Length:**

Between 0.660 inches and 0.785 inches

# Body Width:

Between 0.220 inches and 0.280 inches

# **Body Height:**

Between 0.140 inches and 0.180 inches

# **Operating Tempurature Range:**

+0.0/+70.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **End Application:**

6625llmc03113 24655

#### **Features Provided:**

Hermetically sealed and monolithic and low power and schottky

#### Inclosure Material:

Ceramic

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

Triple 3 input

## **Design Function And Quantity:**

3 gate, nor

#### **Case Outline Source And Designator:**

T0-116 joint electron device engineering council

#### **Time Rating Per Chacteristic:**

13.00 nanoseconds propagation delay time, low to high level output and 13.00 nanoseconds propagation delay time, high to low level output

# Shelf Life:

N/a

#### Unit Of Measure:

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#### **Demilitarization:**

Yes - demil/mli

#### Fiig:

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