NSN 5962-01-139-5785

Memory Microcircuit - Page 1 of 1



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Body Length:

Between 1.235 inches and 1.285 inches

Body Width:

Between 0.515 inches and 0.600 inches

Body Height:

Between 0.145 inches and 0.187 inches

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+125.0 degrees celsius

End Application:

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Features Provided:

Hermetically sealed and erasable and ultraviolet erasable and programmed

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

14 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

5.0 volts power source

Time Rating Per Chacteristic:

450.00 nanoseconds propagation delay time, low to high level output

Memory Device Type:

Rom

Memory Capacity:

Unknown

Terminal Type And Quantity:

24 printed circuit

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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