NSN 5962-01-141-2601

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Body Length:
Between 0.750 inches and 0.785 inches
Body Width:
Between 0.220 inches and 0.310 inches
Body Height:
0.185 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
W/clear and w/preset and positive edge triggered
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 5 input
Design Function And Quantity:
2 flip-flop, j-k, clocked
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
25.00 nanoseconds propagation delay time, low to high level output and 40.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
09344-pc73604 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
16 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli

Fiig:

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