# NSN 5962-01-143-6833

Memory Microcircuit - Page 1 of 2



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# **Body Length:**

Between 1.235 inches and 1.285 inches

# Body Width:

Between 0.515 inches and 0.600 inches

# **Body Height:**

Between 0.145 inches and 0.175 inches

# Maximum Power Dissipation Rating:

525.0 milliwatts

## **Operating Tempurature Range:**

-10.0/+80.0 degrees celsius

## Storage Tempurature Range:

-65.0/+125.0 degrees celsius

## **End Application:**

Bit verificat 82577

## Features Provided:

Erasable and programmable and static operation and high speed

## Inclosure Material:

Ceramic

## Inclosure Configuration:

Dual-in-line

# Output Logic Form:

Transistor-transistor logic

## Input Circuit Pattern:

11 input

# **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

6.0 volts power source

## **Time Rating Per Chacteristic:**

650.00 nanoseconds propagation delay time, low to high level output

#### Memory Device Type:

Rom

#### **Test Data Document:**

82577-1090135 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### **Terminal Type And Quantity:**

24 printed circuit

#### Shelf Life:

N/a

#### Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

# NSN 5962-01-143-6833

Memory Microcircuit - Page 2 of 2

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