# NSN 5962-01-145-3257

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## **Body Length:**

Between 1.235 inches and 1.290 inches

#### **Body Width:**

0.560 inches

# **Body Height:**

Between 0.140 inches and 0.180 inches

## **Operating Tempurature Range:**

+0.0/+75.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **End Application:**

4920-01-049-0223 e/i fscm 88818

#### **Features Provided:**

Hermetically sealed and monolithic and positive outputs and bipolar and schottky and 3-state output

#### **Inclosure Material:**

# Ceramic and glass

Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

16 input

#### **Terminal Surface Treatment:**

Solder

#### Voltage Rating And Type Per Characteristic:

5.5 volts power source

#### **Time Rating Per Chacteristic:**

60.00 nanoseconds propagation delay time, low to high level output and 60.00 nanoseconds propagation delay time, high to low level output

#### Memory Device Type:

Rom

#### **Memory Capacity:**

Unknown

#### **Test Data Document:**

28480-1816-0912 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## Terminal Type And Quantity:

24 printed circuit

# Shelf Life:

N/a

#### Unit Of Measure:

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# Demilitarization:

Yes - demil/mli

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