NSN 5962-01-145-8945

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End Application:

Aim-54c guided missile

Features Provided:

Monolithic and burn in and programmable

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

10 input

Time Rating Per Chacteristic:

90.00 nanoseconds propagation delay time, low to high level output and 90.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

82577-3567352-1 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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