## NSN 5962-01-146-1327

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| Body Length:                   |   |
|--------------------------------|---|
| 0.808 inches                   |   |
| Body Width:                    |   |
| 0.257 inches                   |   |
| Body Height:                   |   |
| 0.204 inches                   |   |
| Maximum Power Dissipation      | Rating:   |
| 800.0 milliwatts               |   |
| Operating Tempurature Rang     | e:  |
| -55.0/+125.0 degrees celsius   |   |
| Storage Tempurature Range      |   |
| 65.0/+150.0 degrees celsius    |   |
| End Application:               |   |
| Cp-1418/tps 97942              |   |
| Features Provided:             |   |
| Bipolar and 3-state output and | w/enable and w/buffered output and monolithic   |
| Inclosure Material:            |   |
| Ceramic                        |   |
| nclosure Configuration:        |   |
| Dual-in-line                   |   |
| Output Logic Form:             |   |
| Transistor-transistor logic    |   |
| nput Circuit Pattern:          |   |
| 3 input                        |   |
| Terminal Surface Treatment:    |   |
| Solder                         |   |
| Voltage Rating And Type Per    | Characteristic:   |
| 5.5 volts power source         |   |
| Time Rating Per Chacteristic   |   |
| 60.00 nanoseconds propagati    | on delay time, low to high level output and 60.00 nanoseconds propagation delay time, high to low level |
| output                         |   |
| Memory Device Type:            |   |
| Rom                            |   |
| Memory Capacity:               |   |
| Unknown                        |   |
| Terminal Type And Quantity:    |   |
| 16 printed circuit             |   |
| Shelf Life:                    |   |
| N/a                            |   |
| Unit Of Measure:               |   |
|                                |   |
| Demilitarization:              |   |
| Yes - demil/mli                |   |

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