# NSN 5962-01-146-2016

Digital Microcircuit - Page 1 of 1



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## **Body Length:**

Between 0.660 inches and 0.785 inches

## Body Width:

Between 0.220 inches and 0.280 inches

## **Body Height:**

Between 0.140 inches and 0.180 inches

# **Maximum Power Dissipation Rating:**

100.0 milliwatts

#### **Operating Tempurature Range:**

+0.0/+75.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+175.0 degrees celsius

#### **End Application:**

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#### Features Provided:

Hermetically sealed and negative edge triggered and w/common clock and w/reset

## Inclosure Material:

Ceramic

#### Inclosure Configuration:

Dual-in-line

## **Output Logic Form:**

Transistor-transistor logic

## Input Circuit Pattern:

8 input

#### **Design Function And Quantity:**

2 flip-flop, j-k

#### **Case Outline Source And Designator:**

T0-116 joint electron device engineering council

#### **Terminal Surface Treatment:**

Solder

#### Voltage Rating And Type Per Characteristic:

5.5 volts power source

#### **Terminal Type And Quantity:**

14 printed circuit

Shelf Life:

N/a

#### Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

# Fiig:

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