NSN 5962-01-147-3386

Digital Microcircuit - Page 1 of 2



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Body Length:
Between 0.745 inches and 0.785 inches
Body Width:
Between 0.240 inches and 0.260 inches
Body Height:
Between 0.120 inches and 0.200 inches
Maximum Power Dissipation Rating:
500.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
End Application:
1430-01-134-6655
Features Provided:
Monolithic and electrostatic sensitive and hermetically sealed
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
8 input
Design Function And Quantity:
6 flip-flop, d-type
Voltage Rating And Type Per Characteristic:
-0.5 volts power source and 18.0 volts power source
Capitance Rating Per Characteristic:
7.50 input picofarads
Time Rating Per Chacteristic:
420.00 nanoseconds propagation delay time, high to low level output and 420.00 nanoseconds propagation delay time, low to high level
output
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
8 printed circuit
Shelf Life:
N/a
Unit Of Measure:

Demilitarization: Yes - demil/mli

NSN 5962-01-147-3386Digital Microcircuit - Page 2 of 2



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