NSN 5962-01-148-3379

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Body Le	- 44
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Between 0.745 inches and 0.770 inches

Body Width:

Between 0.240 inches and 0.260 inches

Body Height:

Between 0.135 inches and 0.150 inches

Maximum Power Dissipation Rating:

500.0 milliwatts

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

End Application:

5820-01-102-3921 80058

Features Provided:

Hermetically sealed and high voltage and medium speed and high impedance

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Complementary-metal oxide-semiconductor logic

Input Circuit Pattern:

6 input

Design Function And Quantity:

1 gate, complementary pair and 3 inverter

Case Outline Source And Designator:

Mo-001-ab joint electron device engineering council

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-0.5 volts power source and 20.0 volts power source

Time Rating Per Chacteristic:

200.00 nanoseconds propagation delay time, low to high level output and 200.00 nanoseconds propagation delay time, high to low level output

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

14 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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