# NSN 5962-01-149-9219

Digital Microcircuit - Page 1 of 1



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# **Body Length:**

Between 0.750 inches and 0.785 inches

# **Body Width:**

Between 0.245 inches and 0.280 inches

# **Body Height:**

0.180 inches

# **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

## Storage Tempurature Range:

-65.0/+150.0 degrees celsius

# **Features Provided:**

Monolithic

## **Inclosure Material:**

Ceramic

#### Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

Quad 2 input

# **Design Function And Quantity:**

4 gate, nand

## Test Data Document:

02101-80802 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;

excludes any specification, standard or other document that may be referenced in a basic governing drawing)

#### Shelf Life:

N/a

# Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

Fiig:

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