NSN 5962-01-155-0668

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Features Provided:
High speed and monolithic and low power and schottky and 3-state output
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
8 input
Design Function And Quantity:
6 buffer
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Terminal Type And Quantity:
18 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:
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