NSN 5962-01-155-1477

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Body Length:
Between 0.245 inches and 0.260 inches
Body Width:
Between 0.245 inches and 0.260 inches
Body Height:
0.100 inches
Maximum Power Dissipation Rating:
30.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and low power and schottky
Inclosure Material:
Ceramic
Inclosure Configuration:
Leadless flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Quad 2 input
Design Function And Quantity:
4 gate, nor
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
5.5 volts power source
Time Rating Per Chacteristic:
30.00 nanoseconds propagation delay time, low to high level output and 30.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
97942-584r976 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc
excludes any specification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And Quantity:
16 leadless
Shelf Life:
N/a
Unit Of Measure:
-
Demilitarization:

Fiig: A458a0

Yes - demil/mli