NSN 5962-01-157-3651

Memory Microcircuit - Page 1 of 1



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Body Length:

Between 1.380 inches and 1.430 inches

Body Width:

Between 0.570 inches and 0.610 inches

Body Height:

Between 0.080 inches and 0.120 inches

Operating Tempurature Range:

-55.0/+85.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

End Application:

Central office type an/ttc-39

Features Provided:

Hermetically sealed and bipolar and w/enable and w/open collector and schottky

Inclosure Material:

Ceramic

Inclosure Configuration:

Dual-in-line

Output Logic Form:

Transistor-transistor logic

Input Circuit Pattern:

17 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

7.0 volts power source

Time Rating Per Chacteristic:

80.00 nanoseconds propagation delay time, low to high level output

Memory Device Type:

Ram

Test Data Document:

80063-sm-a-838094 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

28 printed circuit

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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