NSN 5962-01-157-4556

Memory Microcircuit - Page 1 of 1



View Online at https://aerobasegroup.com/nsn/5962-01-157-4556 **Body Length:** Between 1.235 inches and 1.285 inches **Body Width:** Between 0.515 inches and 0.600 inches **Body Height:** Between 0.140 inches and 0.185 inches **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+125.0 degrees celsius **Features Provided:** Programmable and electrostatic sensitive and ultraviolet erasable **Inclosure Material:** Ceramic **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 14 input **Terminal Surface Treatment:** Solder **Voltage Rating And Type Per Characteristic:**

6.0 volts power source

Time Rating Per Chacteristic:

450.00 nanoseconds propagation delay time, low to high level output

Memory Device Type:

Rom

Test Data Document:

94987-148965 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Terminal Type And Quantity:

24 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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