## NSN 5962-01-157-9655

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Body Length:
Between 0.240 inches and 0.280 inches
Body Width:
Between 0.240 inches and 0.260 inches
Body Height:
0.070 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
High speed and hermetically sealed
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Flat pack
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Dual 2 input
Design Function And Quantity:
2 driver, switch, field effect transistor
Case Outline Source And Designator:
T0-86 joint electron device engineering council
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
36.0 volts power source
Test Data Document:
82577-932147 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
14 flat leads
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig:

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