NSN 5962-01-158-8862

Memory Microcircuit - Page 1 of 1



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Body Length:
Between 1.380 inches and 1.430 inches
Body Width:
Between 0.580 inches and 0.610 inches
Body Height:
Between 0.080 inches and 0.120 inches
Maximum Power Dissipation Rating:
1000.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Hermetically sealed and burn in and programmable and w/open collector
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
17 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
80.00 nanoseconds propagation delay time, low to high level output and 80.00 nanoseconds propagation delay time, high to low level
output
Memory Device Type:
Pal
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
28 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Vas - demil/mli

Fiig:

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