## NSN 5962-01-158-8947

Memory Microcircuit - Page 1 of 1



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| Body Length:   |  |
|--|--|
| 1.290 inches   |  |
| Body Width:  |  |
| Between 0.500 inches and 0.610 inches                                |  |
| Body Height:   |  |
| 0.210 inches   |  |
| Operating Tempurature Range:   |  |
| 55.0/+125.0 degrees celsius  |  |
| Storage Tempurature Range:   |  |
| 65.0/+150.0 degrees celsius  |  |
| Features Provided:   |  |
| Hermetically sealed and programmable and bipolar and high impeda     | nce  |
| nclosure Material:   |  |
| Ceramic and glass  |  |
| nclosure Configuration:  |  |
| Dual-in-line   |  |
| Dutput Logic Form:   |  |
| Fransistor-transistor logic  |  |
| nput Circuit Pattern:  |  |
| 14 input   |  |
| Case Outline Source And Designator:                                  |  |
| D-3 mil-m-38510  |  |
| Ferminal Surface Treatment:  |  |
| Solder   |  |
| Voltage Rating And Type Per Characteristic:                          |  |
| 7.0 volts power source   |  |
| Memory Device Type:  |  |
| Rom  |  |
| Fest Data Document:  |  |
| 96906-mil-std-883 standard (includes industry or association standar | ds, individual manufactureer standards, etc.). |
| Ferminal Type And Quantity:  |  |
| 24 printed circuit   |  |
| Shelf Life:  |  |
| N/a  |  |
| Jnit Of Measure:   |  |
| -  |  |
| Demilitarization:  |  |
| Yes - demil/mli  |  |
| Filg:  |  |
| A458a0   |  |