# NSN 5962-01-159-6881

Memory Microcircuit - Page 1 of 1



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## **Body Length:**

Between 1.435 inches and 1.485 inches

## **Body Width:**

Between 0.515 inches and 0.600 inches

## **Body Height:**

0.180 inches

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **End Application:**

Test set, countermeasures, type an/usm-464

#### **Features Provided:**

Hermetically sealed and monolithic and programmable and ultraviolet erasable

#### **Inclosure Material:**

# Ceramic and glass

## Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

N-type metal oxide-semiconductor logic

#### Input Circuit Pattern:

15 input

## **Terminal Surface Treatment:**

Solder

#### Voltage Rating And Type Per Characteristic:

6.0 volts power source

#### **Time Rating Per Chacteristic:**

250.00 nanoseconds propagation delay time, low to high level output

#### Memory Device Type:

Rom

#### **Test Data Document:**

94117-314057 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.;

excludes any specification, standard or other document that may be referenced in a basic governing drawing)

## Terminal Type And Quantity:

28 printed circuit

#### Shelf Life:

N/a

## Unit Of Measure:

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## Demilitarization:

Yes - demil/mli

#### Fiig:

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