## NSN 5962-01-160-7137

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## **Body Length:** 1.290 inches **Body Width:** Between 0.500 inches and 0.610 inches **Body Height:** 0.210 inches **Maximum Power Dissipation Rating:** 1.0 watts **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **End Application:** Radar system an/fps-117 **Features Provided:** Monolithic and bipolar and programmable and schottky and hermetically sealed **Inclosure Material:** Ceramic and glass Inclosure Configuration: Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 13 input **Case Outline Source And Designator:** D-3 mil-m-38510 **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: 5.5 volts power source **Time Rating Per Chacteristic:** 90.00 nanoseconds propagation delay time, low to high level output and 90.00 nanoseconds propagation delay time, high to low level output Memory Device Type: Rom **Test Data Document:** 03538-7849972 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing, etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing) **Terminal Type And Quantity:** 24 printed circuit Shelf Life: N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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