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Operating Tempurature Range:

-25.0/+125.0 degrees celsius

Inclosure Configuration:

Flat pack

Output Logic Form:

Complementary-oxidized-silicon logic

Input Circuit Pattern:

3 stage

Design Function And Quantity:

1 driver, memory address

Test Data Document:

98230-0n520437 drawing (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,

etc.; excludes any specification, standard or other document that may be referenced in a basic governing drawing)

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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