NSN 5962-01-165-1679

Memory Microcircuit - Page 1 of 1



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Features	Dras	idad.
reatures	PIO	/iaea:

Erasable and programmed and electrostatic sensitive

Inclosure Configuration:

Dual-in-line

Terminal Surface Treatment:

Solder

Memory Device Type:

Rom

Shelf Life:

N/a

Unit Of Measure:

Demilitarization:

Yes - demil/mli

Fiig:

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