# NSN 5962-01-169-8025

Memory Microcircuit - Page 1 of 1



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## **Body Length:**

Between 1.060 inches and 1.100 inches

### **Body Width:**

0.400 inches

## **Body Height:**

Between 0.145 inches and 0.175 inches

#### **Operating Tempurature Range:**

-55.0/+125.0 degrees celsius

#### Storage Tempurature Range:

-65.0/+150.0 degrees celsius

#### **End Application:**

Trainer, simulator, electronic warfare an/alq-t5

#### **Features Provided:**

Hermetically sealed and bipolar and 3-state output and schottky

#### **Inclosure Material:**

## Ceramic and glass

Inclosure Configuration:

Dual-in-line

#### **Output Logic Form:**

Transistor-transistor logic

#### Input Circuit Pattern:

20 input

### **Terminal Surface Treatment:**

Solder

## Voltage Rating And Type Per Characteristic:

-0.3 volts power source and 7.0 volts power source

#### Time Rating Per Chacteristic:

45.00 nanoseconds propagation delay time, low to high level output

#### Memory Device Type:

Rom

#### **Test Data Document:**

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

#### **Terminal Type And Quantity:**

22 printed circuit

#### Shelf Life:

N/a

### Unit Of Measure:

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### Demilitarization:

Yes - demil/mli

## Fiig:

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