NSN 5962-01-171-2151

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Body Length:
0.796 inches
Body Width:
Between 0.220 inches and 0.310 inches
Body Height:
Between 0.140 inches and 0.185 inches
Maximum Power Dissipation Rating:
400.0 milliwatts
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
End Application:
Missile, tomahawk; countermeasure set, an/slq-32; missile, air-to-air, sparrow (aim-7); torpedo, mk 48; radar, series an/sps-4 (v); tes
station, oq-290(v)msm; missile, air-to-air, phoenix (aim-54); close in weapon system (ciws-phalanx); support equipment, b-1 aircraft
Features Provided:
Monolithic
Inclosure Material:
Silicon
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Quad 2 input
Criticality Code Justification:
Feat
Design Function And Quantity:
4 buffer, nand
Product Name:
Quadruple, 2-input positive nand buffer
Voltage Rating And Type Per Characteristic:
7.0 volts power source and -0.5 volts power source and 5.5 volts input and -1.5 volts input
Special Features:
Max pwr is 100 mw per buffer and weapon system essential
Test Data Document:
81349-mil-m-38510/3 specification (includes engineering type bulletins, brochures, etc., that reflect specification type data in specification
format; excludes commercial catalogs, industry directories, and similar trade publications, reflecting general type data on certain
environmental and performance requirements and test conditions that are shown as "typical", "average", "", etc.).
Terminal Type And Quantity:
14 pin
Shelf Life:
N/a
Unit Of Measure:

Vec - demil/mli

Demilitarization:

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