## NSN 5962-01-171-5581

Memory Microcircuit - Page 1 of 1



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Body Length:	
0.520 inches	
Body Width: 0.390 inches	
Body Height:	
0.085 inches	as Dispinction Bating
715.0 milliwatts	er Dissipation Rating:
	purature Range:
-55.0/+125.0 de	-
Storage Temp	
-65.0/+150.0 de	-
Features Provi	
	grammable and 3-state output
Inclosure Mate	rial:
Ceramic	
Inclosure Conf	iguration:
Flat pack	
Output Logic F	
Transistor-trans	
Input Circuit Pa	attern:
12 input	
Terminal Surfa	ce Treatment:
Solder	
	And Type Per Characteristic:
•	r source and 7.0 volts power source
Time Rating Pe	
125.00 nanosec	conds propagation delay time, low to high level output and 125.00 nanoseconds propagation delay time, high to low level
output	
Memory Device	е Туре:
Rom	
Test Data Docu	ument:
82577-932892 \$	standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type	And Quantity:
18 flat leads	
Shelf Life:	
N/a	
Unit Of Measur	e:
Demilitarizatio	n:
Yes - demil/mli	
Fiig:	
A458a0	