NSN 5962-01-171-5593

Memory Microcircuit - Page 1 of 1



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Body Length:	
0.520 inches	
Body Width:	
0.390 inches	
Body Height:	
0.085 inches	
Maximum Power	issipation Rating:
715.0 milliwatts	
Operating Tempur	ature Range:
-55.0/+125.0 degree	es celsius
Storage Tempurat	ure Range:
-65.0/+150.0 degree	es celsius
Features Provided	:
Bipolar and program	nmable and 3-state output
Inclosure Material	
Ceramic	
Inclosure Configu	ration:
Flat pack	
Output Logic Forn	1:
Transistor-transisto	logic
Input Circuit Patte	rn:
12 input	
Terminal Surface	Freatment:
Solder	
Voltage Rating An	d Type Per Characteristic:
-0.5 volts power so	urce and 7.0 volts power source
Time Rating Per C	hacteristic:
125.00 nanosecond	s propagation delay time, low to high level output and 125.00 nanoseconds propagation delay time, high to low level
output	
Memory Device Ty	pe:
Rom	
Test Data Docume	nt:
82577-932892 stan	dard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And	I Quantity:
18 flat leads	
Shelf Life:	
N/a	
Unit Of Measure:	
Demilitarization:	
Yes - demil/mli	
Fiig:	
A458a0	