## NSN 5962-01-171-7531

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## **Body Length:** 1.290 inches **Body Width:** Between 0.500 inches and 0.610 inches **Body Height:** 0.210 inches **Maximum Power Dissipation Rating:** 575.0 milliwatts **Operating Tempurature Range:** -55.0/+125.0 degrees celsius Storage Tempurature Range: -65.0/+150.0 degrees celsius **Features Provided:** Monolithic and bipolar and hermetically sealed and programmable and w/open collector **Inclosure Material:** Ceramic and glass **Inclosure Configuration:** Dual-in-line **Output Logic Form:** Transistor-transistor logic **Input Circuit Pattern:** 8 input **Case Outline Source And Designator:** D-3 mil-m-38510 **Terminal Surface Treatment:** Solder Voltage Rating And Type Per Characteristic: -0.5 volts power source and 7.0 volts power source **Time Rating Per Chacteristic:** 140.00 nanoseconds propagation delay time, low to high level output and 140.00 nanoseconds propagation delay time, high to low level output Memory Device Type: Rom **Test Data Document:** 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). **Terminal Type And Quantity:** 24 printed circuit Shelf Life: N/a Unit Of Measure:

Demilitarization: Yes - demil/mli

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**Fiig:** A458a0

