NSN 5962-01-171-8584

Memory Microcircuit - Page 1 of 1



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Body Length:
Between 1.235 inches and 1.285 inches
Body Width:
Between 0.515 inches and 0.600 inches
Body Height:
Between 0.145 inches and 0.175 inches
Maximum Power Dissipation Rating:
525.0 milliwatts
Operating Tempurature Range:
+0.0/+70.0 degrees celsius
Storage Tempurature Range:
-65.0/+125.0 degrees celsius
End Application:
4920-01-084-7157 test station, an
Features Provided:
Hermetically sealed and programmable and ultraviolet erasable and 3-state output
Inclosure Material:
Ceramic and glass
Inclosure Configuration:
Dual-in-line
Output Logic Form:
N-type metal oxide-semiconductor logic
Input Circuit Pattern:
13 input
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
-0.3 volts power source and 6.0 volts power source
Time Rating Per Chacteristic:
450.00 nanoseconds propagation delay time, low to high level output
Memory Device Type:
Rom
Terminal Type And Quantity:
24 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli

Fiig:

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