## NSN 5962-01-172-1485

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| Body Length:                                       |
|--|
| 0.240 inches                                       |
| Body Width:  |
| 0.265 inches                                       |
| Body Height:                                       |
| Between 0.050 inches and 0.080 inches              |
| Operating Tempurature Range:                       |
| -55.0/+125.0 degrees celsius                       |
| Storage Tempurature Range:                         |
| -65.0/+150.0 degrees celsius                       |
| Features Provided:                                 |
| Burn in and electrostatic sensitive and monolithic |
| Inclosure Material:                                |
| Ceramic  |
| Inclosure Configuration:                           |
| Flat pack  |
| Output Logic Form:                                 |
| Transistor-transistor logic                        |
| Input Circuit Pattern:                             |
| Quad 2 input                                       |
| Criticality Code Justification:                    |
| Feat   |
| Design Function And Quantity:                      |
| 4 gate, or   |
| Terminal Surface Treatment:                        |
| Solder   |
| Voltage Rating And Type Per Characteristic:        |
| 7.0 volts power source                             |
| Special Features:                                  |
| Esd  |
| Terminal Type And Quantity:                        |
| 14 flat leads                                      |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:                                   |
|  |
| Demilitarization:                                  |
| Yes - demil/mli                                    |
| Fiig:  |
| A458a0   |
|  |
|  |