NSN 5962-01-176-3078

Memory Microcircuit - Page 1 of 1



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Body Length:

1.460 inches

Body Width:

0.610 inches

Body Height:

Between 0.140 inches and 0.185 inches

Operating Tempurature Range:

-55.0/+125.0 degrees celsius

Storage Tempurature Range:

-65.0/+150.0 degrees celsius

End Application:

Landing craft air cushion (lcac), tarawa class lha, nimitz class cvn, forrestal class cv, emory s. Land, wasp class lhd. B-1b aircraft. 2m/ate microminiauture automatic test equipment.

Features Provided:

Programmable and hermetically sealed and 3-state output and ultraviolet erasable

Inclosure Material:

Ceramic and glass

Inclosure Configuration:

Dual-in-line

Output Logic Form:

N-type metal oxide-semiconductor logic

Input Circuit Pattern:

16 input

Terminal Surface Treatment:

Solder

Voltage Rating And Type Per Characteristic:

-0.6 volts power source and 22.0 volts power source

Time Rating Per Chacteristic:

300.00 nanoseconds propagation delay time, low to high level output and 300.00 nanoseconds propagation delay time, high to low level output

Memory Device Type:

Rom

Test Data Document:

96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).

Terminal Type And Quantity:

28 printed circuit

Shelf Life:

N/a

Unit Of Measure:

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Demilitarization:

Yes - demil/mli

Fiig:

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