NSN 5962-01-178-2798

Memory Microcircuit - Page 1 of 1



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Body L	ength:
2.020 ir	iches
Body V	/idth:
0.890 ir	iches
Body H	eight:
0.170 ir	iches
Maximu	Im Power Dissipation Rating:
280.0 m	illiwatts
Operati	ng Tempurature Range:
-55.0/+′	25.0 degrees celsius
Storage	e Tempurature Range:
-65.0/+′	150.0 degrees celsius
Feature	es Provided:
Hermet	cally sealed and electrostatic sensitive and w/enable
Inclosu	re Material:
Cerami	
Inclosu	re Configuration:
Dual-in-	line
Output	Logic Form:
Comple	mentary-metal oxide-semiconductor logic
Input C	ircuit Pattern:
28 inpu	t de la constante d
Termin	al Surface Treatment:
Solder	
Voltage	e Rating And Type Per Characteristic:
8.3 volt	s power source
Memor	y Device Type:
Ram	
Test Da	ita Document:
96906-r	nil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Termin	al Type And Quantity:
40 print	ed circuit
Shelf L	ife:
N/a	
Unit Of	Measure:
Demilit	arization:
Yes - de	emil/mli
Fiig:	
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