NSN 5962-01-180-1187

Memory Microcircuit - Page 1 of 1



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Body Length:	
0.870 inches	
Body Width:	
0.250 inches	
Body Height:	
0.130 inches	
Operating Tempurature	e Range:
-0.0/+70.0 degrees celsi	us
Storage Tempurature F	Range:
-65.0/+150.0 degrees ce	lsius
Features Provided:	
Bipolar and programmat	ble and schottky and w/enable and 3-state output
Inclosure Configuration	n:
Dual-in-line	
Output Logic Form:	
Transistor-transistor logi	c
Input Circuit Pattern:	
10 input	
Terminal Surface Treat	ment:
Solder	
Voltage Rating And Ty	pe Per Characteristic:
7.0 volts power source	
Time Rating Per Chacte	eristic:
55.00 nanoseconds pro	pagation delay time, low to high level output and 55.00 nanoseconds propagation delay time, high to low level
output	
Memory Device Type:	
Rom	
Terminal Type And Qua	antity:
16 printed circuit	
Shelf Life:	
N/a	
Unit Of Measure:	
Demilitarization:	
No	
Fiig:	
A458a0	