## NSN 5962-01-180-1250

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Body Length:
Between 0.750 inches and 0.785 inches
Body Width:
Between 0.245 inches and 0.271 inches
Body Height:
0.185 inches
Operating Tempurature Range:
-55.0/+125.0 degrees celsius
Storage Tempurature Range:
-65.0/+150.0 degrees celsius
Features Provided:
Monolithic and hermetically sealed
Inclosure Material:
Ceramic
Inclosure Configuration:
Dual-in-line
Output Logic Form:
Transistor-transistor logic
Input Circuit Pattern:
Quad 2 input
Design Function And Quantity:
4 gate, nor
Terminal Surface Treatment:
Solder
Voltage Rating And Type Per Characteristic:
7.0 volts power source
Time Rating Per Chacteristic:
10.00 nanoseconds propagation delay time, low to high level output and 10.00 nanoseconds propagation delay time, high to low level
output
Test Data Document:
96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.).
Terminal Type And Quantity:
14 printed circuit
Shelf Life:
N/a
Unit Of Measure:
Demilitarization:
Yes - demil/mli
Fiig: