## NSN 5962-01-181-3832

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| Body Length:   |
|--|
| 1.105 inches   |
| Body Width:  |
| 0.393 inches   |
| Body Height:   |
| 0.125 inches   |
| Maximum Power Dissipation Rating:  |
| 500.0 milliwatts   |
| Operating Tempurature Range:   |
| -55.0/+125.0 degrees celsius   |
| Storage Tempurature Range:   |
| -65.0/+150.0 degrees celsius   |
| Features Provided:   |
| Electrically alterable and erasable and w/clock  |
| Inclosure Material:  |
| Ceramic  |
| Inclosure Configuration:   |
| Dual-in-line   |
| Output Logic Form:   |
| N-type metal oxide-semiconductor logic   |
| Input Circuit Pattern:   |
| 19 input   |
| Terminal Surface Treatment:  |
| Solder   |
| Voltage Rating And Type Per Characteristic:  |
| 0.3 volts power source   |
| Memory Device Type:  |
| Rom  |
| Test Data Document:  |
| 96906-mil-std-883 standard (includes industry or association standards, individual manufactureer standards, etc.). |
| Terminal Type And Quantity:  |
| 22 printed circuit   |
| Shelf Life:  |
| N/a  |
| Unit Of Measure:   |
|  |
| Demilitarization:  |
| Yes - demil/mli  |
| Fiig:  |
| A458a0   |