## NSN 5962-01-182-1589

Memory Microcircuit - Page 1 of 1



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Body Length:	
1.010 inches	
Body Width:	
0.300 inches	
Body Height:	
0.170 inches	
Maximum Power Dis	sipation Rating:
850.0 milliwatts	
Operating Tempurat	ure Range:
-55.0/+125.0 degrees	celsius
Storage Tempuratur	e Range:
65.0/+150.0 degrees	celsius
Features Provided:	
Bipolar and programr	able and programmed and hermetically sealed
Inclosure Material:	
Ceramic or glass or n	etal
Inclosure Configura	ion:
Dual-in-line	
Output Logic Form:	
Transistor-transistor I	gic
nput Circuit Pattern	
10 input	
Terminal Surface Tr	eatment:
Solder	
Voltage Rating And	Type Per Characteristic:
7.0 volts power sour	e
Time Rating Per Cha	cteristic:
120.00 nanoseconds	propagation delay time, low to high level output
Memory Device Typ	r.
Rom	
Test Data Documen	
89954-144a9552 dra	ving (this is the basic governing drawing, such as a contractor drawing, original equipment manufacturer drawing,
etc.; excludes any sp	ecification, standard or other document that may be referenced in a basic governing drawing)
Terminal Type And	Quantity:
20 printed circuit	
Shelf Life:	
N/a	
Unit Of Measure:	
Demilitarization:	
Yes - demil/mli	
Fiig:	
A458a0	